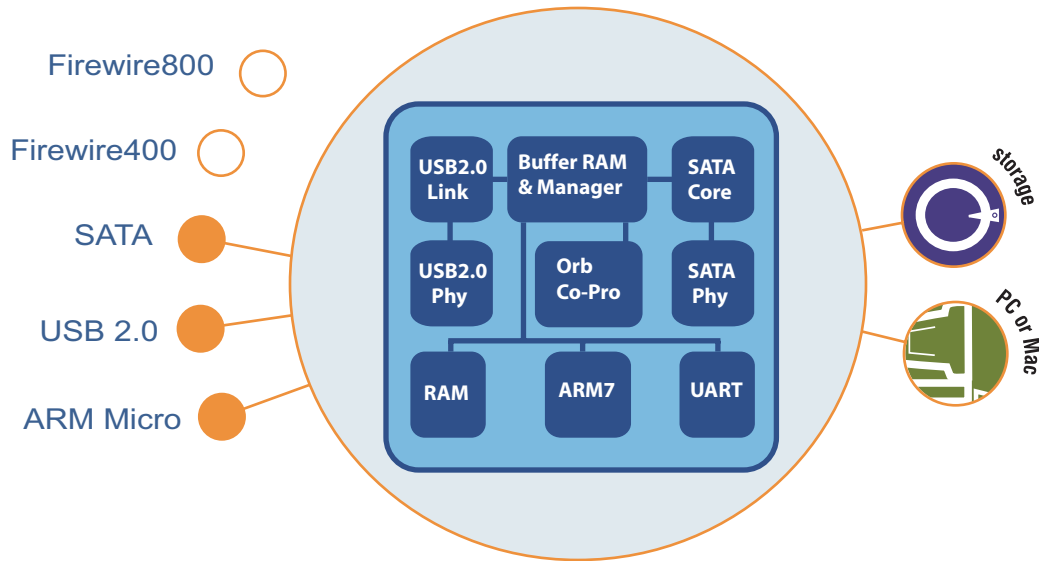


# OXU921S

## USB2.0 to SATA Bridge



The Oxford OXU921S is a complete solution for bridging between SATA disks and USB2.0. Typically the chip is used for creating external storage solutions which connect to a PC or Apple Mac via the USB2.0 port. The U921S contains a full USB2.0 link and Phy minimising external components. The SATA interface is also fully integrated, and compatible with the latest generation Serial-ATA drives.

### Key features

Integrated SATA core & Phy operating at 1.5Ghz with 150Mbytes/s data rates

Supports SATA II port multiplier specification

Integrated 480Mbps USB2.0 link & Phy

Comes complete with Firmware that supports USB mass storage protocol (supported as standard by Windows and Apple Mac)

Industry renowned ARM7, with plenty of spare MIPs for product specific software

Field upgradable firmware

Integral close coupled 8kbyte uP RAM  
6Kbyte RAM USB data cache

12 GPIOs allow easy product customisation

128 pin QFP

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## USB2.0 to SATA Bridge



### USB2.0

The OXU921S supports both Full and High-Speed USB modes. It's high performance FIFO manager together with high speed architecture exceed the throughput capability of today's PCs, ensuring it keeps pace as they continue to increase in speed.

### SATA Interface

The SATA interface allows external storage products to follow the current transition of hard disk drives from Parallel ATA to Serial ATA.

### Embedded ARM processor

Managing the data flow through the OXU921S, the on-chip ARM7TDMI enables a whole new series of standalone consumer electronic products to be developed in a simple C/C++ programming environment.

### Development tools and custom solutions

For external MAC and PC storage solutions, Oxford Semiconductor offers a selection of reference designs, development code, drivers, and evaluation kits. Comprehensive application notes shorten learning curves and simplify implementation.

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